



CORRIGENDUM

Reference Tender Notice No. 114/2018 and corrigendum dated 16.04.2018 for the supply of HPC system due on 10.05.2018. The following changes have been made in the specifications:

Technical Part:

1. Parallel File System - Para -2: 6 GB/s or higher should be replaced by minimum 12 GB/s write throughput in HA mode (Rest of the configuration remain same)
2. Communication Network – A. Primary Communication Network: Minimum 264-port, 100Gbps, Non-blocking, Switching Fabric (Mellanox EDR InfiniBand or Intel®Omni-Path) with embedded Subnet Manager for 264 devices or more and with redundant power supply/supplies. (Rest of the configuration remain same)
3. Communication Network – B. Secondary Communication Network: Gigabit Ethernet Switch should be replaced by 10G, Qty- 6 Sets (Rest of the configuration remain same)
4. Software/Installations: Wherever required the vendor has to go for "Commercially available and supported cluster management software/licences".
5. Power Supplies: hot-pluggable and N+N redundant, 80 PLUS Titanium or better to replaced by Power Supplies: hot-pluggable and N+N redundant, 80 PLUS 94% efficiency or better. (Rest of the configuration remain same)
6. Part-II and III– Cooling / Electrical Solution Infrastructure-Page No.10/23 : Vendor(s) are strongly recommended to visit the site to give there deviations if any regarding cooling/electrical solutions.
7. OS: Centos (prefered)
8. For compute nodes the disk size should be 2 x 240GB MLC SATA SSD (rest of the configuration remains same)
9. For Master Node and Compute Node: Memory size should be read as “192GB DDR4-2666 with ECC or higher in balanced configuration populating all memory channels. Upgradable to at least 512GB”(Rest remains same)

General Terms and Conditions: Modification and addendum to be done are given below, remaining Terms and Conditions remains same

10. The OEM and bidder should have presence in the Top Supercomputers - India list (maintained by SERC, IISc, Bangalore <http://topsupercomputersindia.iisc.ernet.in>) continuously for last three years -- to be removed*.
11. The OEM/Bidder should have installed at least 5 compute clusters in India in last three years. Details of these previous installations must be provided. In addition, vendor should provide a guarantee for clustering and also for application software integration.
12. OEM has to feature in the top 500 supercomputer sites and should have had at least one entry in top500.org over the past 3 years.
13. The OEM/bidder should have installed at least one supercomputer of minimum 200 nodes in India. The same should have been successfully supported (post-installation) at least for a period of three years. Provide a certificate from the customer to this effect.
14. The OEM/bidder has to provide power consumption for 5 years and it will be evaluated and corresponding cost will be taken into consideration during the financial bid evaluation.



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15. The OEM/bidder should have installed at least three supercomputers in India with Infiniband/equivalent or better interconnect.
16. The OEM/Bidder must have their sales/service operations in India for last 05 years and the team structure with call logging details and escalation matrix has to be provided.
17. The proposed cluster solution must be delivered to IIT Ropar within 12 - 16 weeks from the opening of LC confirmed LC.
18. 5 years warranty followed by AMC of 2 years (with spares and services) further followed by extended AMC of 3 years (only services). (Total 10 years)
19. The deadline of tender should be extended to 10-05-2018

Benchmarking:

20. Benchmark 1: Quantum ESPRESSO

Scalability over processors should be tested and reported over the following:

- (1) 1 node (with 32 cores or more)
- (2) 2 nodes (with 64 cores or more)
- (3) 3 nodes (with 96 cores or more)
- (4) 4 nodes (with 128 cores or more)
- (5) 8 nodes (with 256 cores or more)

21. Benchmark 2 : ALLPATHS-LG (for high memory SMP node) -- To be Removed**

*** This will make more participants to bid.**

**** As our tender does not mention any high memory SMP system nor the quantity.**

Bidders are requested to submit their bids as per revised Technical Specifications. Those who have already sent/submitted their bids can revise the same clearly indicating "Revised Bid for HPC System" on sealed cover.

Timings and all other details and terms & conditions remain the same.

Registrar