

**List of shortlisted candidates in the Department of Electrical Engineering (VLSI Design), IIT Ropar (Semester-II of Academic Year 2022-2023)
Special Drive for OBS/SC/ST Candidates**

PhD interview date: 13th April, 2023

Sr. No.	SID	Full name	Programme
1	80953	MOHD SAQIB	Regular
2	80980	BAYYA RADHA KUMARI	Regular
3	80999	MANOJ KUMAR	Regular
4	81047	DINESH SINGH	Regular
5	81056	MAYA CHANDRAKAR	Regular
6	81233	NIKITA CHOUDHARY	Regular
7	81234	KAKALI ROY	Regular
8	81257	PRIYANKA	Regular
9	81323	KUNAL SINGH	Regular
10	81361	SARVESH KUMAR PRAJAPATI	Regular
11	81370	BINANDA KISHORE MONDAL	Regular
12	81383	PAMINDER SINGH	Regular
13	81384	MOHD ASIF ALI	Regular
14	81386	VINOD KUMAR NETAD	Regular
15	81445	VIKASH KUMAR	Regular

- **Regular PhD Criteria:**

1. Applied to PhD in Electrical Engineering (Microelectronics and VLSI Design) program
2. Cut-off marks criteria:
 - B.Tech : 65% (6.5 CGPA) or M.Tech 65% (6.5 CGPA) [For GEN/OBC/EWS]
 - B.Tech : 60% (6.0 CGPA) or M.Tech 60% (6.0 CGPA) [For SC/ST/PWD]
4. In the case of B.Tech. degree only:
 - Cut-off marks criteria:
 - B.Tech : 80% (8.0 CGPA) [For GEN/OBC/EWS]
 - B.Tech : 75% (7.5 CGPA) [For SC/ST/PWD]
 - Valid GATE score for candidates other than IITs

NOTE: Any applicant who satisfies the above-mentioned criteria but his/her name is not on the list can also appear for written and/or personal interviews on the first day of the interviews.

Mode of examination: Written Test and/or Personal Interview in Physical Mode at IIT Ropar (Offline). Reporting time 9:00 AM.