



**भारतीय प्रौद्योगिकी संस्थान रोपड़**  
**INDIAN INSTITUTE OF TECHNOLOGY ROPAR**  
नंगल रोड, रूपनगर, पंजाब-140001/Nangal Road, Rupnagar, Punjab-140001  
Ph. 01881-242193, e-mail: purchase@iitrpr.ac.in

No. 1150-18/CH-10098/ISIRD/PS

08.08.2018

**CORRIGENDUM**

With reference to tender notice No. 1150-18/CH-10098/ISIRD/PS dated 12.07.2018 for HPC System and UPS. The specifications have been changed as under:

**A) Head Node: 1 No.**

Item	Description of requirement
Processor	2 x Intel® Xeon processors (24-Core, 2.1GHz)
Chipset	Intel C621 or higher
Memory	384 GB or higher DDR4, registered, ECC, 2,666 MHz scalable to 1536GB
HDD	40TB usable space using 4TB/ 6TB/ 8TB 7200RPM Enterprise SATA hot-pluggable HDDs, at least 8x3.5” Hot-swap HDD bays or more
RAID	On board or AOC Hardware SAS RAID Controller with RAID levels 0,1, 10, 5 and 50 support and at least 2 GB cache
Optical Drive	DVD-RW drive
Expansion slots	6 or more PCIe Gen3 slots (3 PCIe 3.0 x8 or more and 2 PCIe 3.0 x16 or more), should support NVIDIA GPU for future

**B) Compute Nodes: 4 Nos.**

Item	Description of requirement
Processor	2 x Intel® Xeon processors (24-Core, 2.1GHz)
Chipset	Intel C621 or higher
Memory	384 GB DDR4 or higher, registered, ECC, 2,666 MHz scalable to 1536GB

**A) GPU Hybrid Compute Nodes: 2 Nos.**

Item	Description of requirement
Processor	2 x Intel® Xeon processors (24-Core, 2.1GHz)



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GPGPU	1 x Nvidia Tesla P100 12Gb GPGPU Cards (supported for 2 GPUs per node )
Chipset	Intel C621 or higher
Memory	384 GB DDR4 or higher, registered, ECC, 2,666 MHz scalable to 1536GB

**Changes made in point number E**

**Additional Storage:** Additional 100 TB rack mountable data storage is required for this set up. The storage will be connected to Head/Master Node and NFS exported to compute nodes. Another NFS server is not required.

**Changes made in point number (H) ( C)**

**Compilers & Libraries:** The bidder should quote for two Intel compilers (academic version). Installation and integration of Intel compilers and standard open source compilers such as GNU Compiler Collection (GCC) and openMPI.

**Changes made in point number 10 of terms and conditions**

- 1) The OEM/bidder should have installed at least three HPC clusters in central government of india funded research/teaching institute in last three years. The same should have been successfully supported (post-installation) at least for a period of three years. Provide a certificate from the customer to this effect.
- 2) OEM has to feature in the top 500 supercomputer sites and should have had at least one entry in top500.org over the past 3 years.

Bidders are requested to submit their bids as per revised specifications and terms & conditions.

**The last date of receipt and opening of Bids is hereby also extended upto 21.08.2018.**

Timings remains the same. Those who have already sent/submitted their bids can revise the same clearly indicating "Revised Bid" on sealed cover. Rest remains the same.

**Registrar**